



Product Information

DATE: 13.May.2009

SAMSUNG TFT-LCD

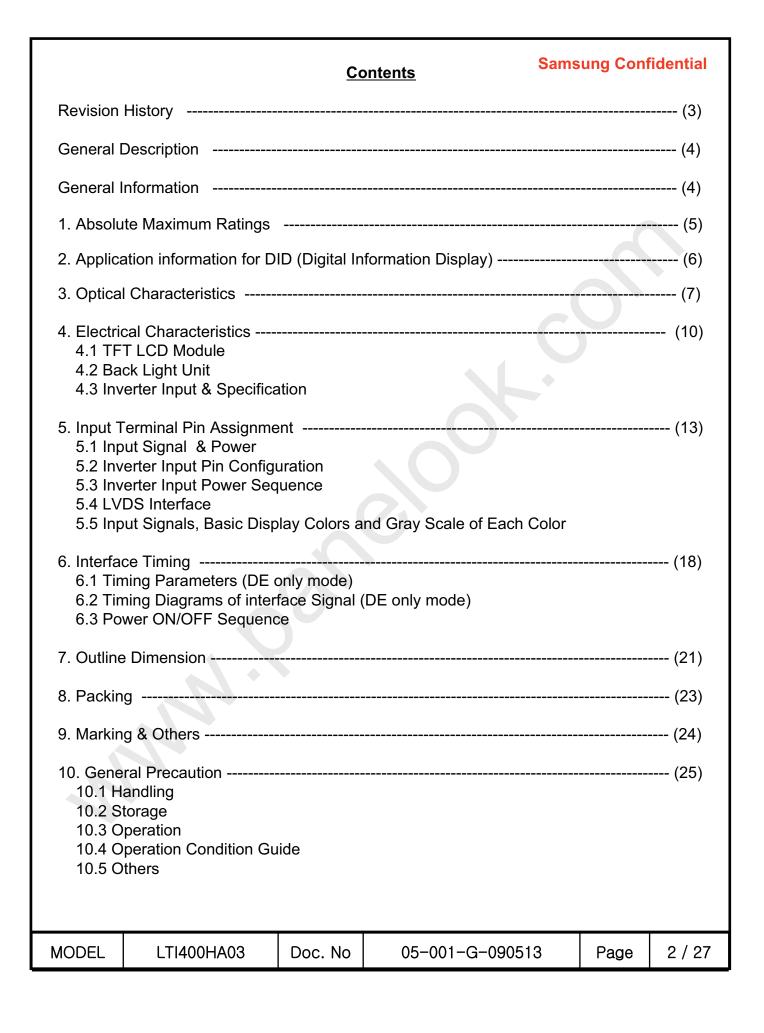
MODEL: LTI400HA03

The Information Described in this Specification is Preliminary and can be changed without prior notice

APPROVED BY	DATE	PREPARED BY	DATE
Je-Hwan Oh	13.May.2009	Yu-Geun Lee	13.May.2009

Application Engineering Part 3, LCD Business
Samsung Electronics Co., LTD.

MODEL	LTI400HA03	Doc. No	05-001-G-090513	Page	1 / 27
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* Revision History

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Date	Rev. No	Page	Summary	
Jan 14, 2009	000	all	First issued	
May 13, 2009	001	21, 22	Mechanical drawing change	

MODEL LTI400HA03 Doc. No 05-001-G-090513 Page 3 / 27



General Description

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Description

LTI400HA03 is a color active matrix liquid crystal display (LCD) that uses amorphous silicon TFT(Thin Film Transistor) as switching components. This model is composed of a TFT LCD panel, a driver circuit and a back light unit. The resolution of a 40.0" is 1920 x 1080 and this model can display up to 16.7 million colors with wide viewing angle of 89° or higher in all directions. This panel is intended to support applications to provide a excellent performance for Flat Panel Display such as Home-alone Multimedia TFT-LCD TV, Display terminals for AV application products, and Digital Information Display (DID).

Features

- RoHS compliance (Pb-free)
- High contrast ratio, High aperture ratio
- SPVA(Super Patterned Vertical Align) mode
- Wide viewing angle (±178°)
- High speed response
- Narrow bezel, Black top chassis
- Landscape / Portrait type compatible
- Wide UXGA (1920 x 1080 pixels) resolution (16:9)
- Low power consumption
- Direct Type 14 CCFTs(Cold Cathode Fluorescent Tube)
- DE(Data Enable) mode
- LVDS (Low Voltage Differential Signaling) interface (2pixel/clock)

General Information

Items	Specification	Unit	Note
Module Size	911.7(W _{TYP}) x 524.2(H _{TYP})	mm	±1.0mm
Wodule Size	59.3(D _{MAX})	mm	
Weight	12,500(Max.)	g	
Pixel Pitch	0.46125(H) x 0.46125(V)	mm	
Active Display Area	885.6(H) x 498.15(V)	mm	
Surface Treatment	Haze 44% , Hard-coating (3H)		
Display Colors	8 bit - 16.7M	colors	
Number of Pixels	1920 x 1080	pixel	
Pixel Arrangement	RGB vertical stripe		
Display Mode	Normally Black		
Luminance of White	700 (Typ.)	cd/m ²	

MODEL	LTI400HA03	Doc. No	05-001-G-090513	Page	4 / 27
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1. Absolute Maximum Ratings

If the condition exceeds maximum ratings, it can cause malfunction or unrecoverable damage to the device.

Iten	Symbol	Min.	Max.	Unit	Note	
Power Supply Voltage		V_{DD}	GND-0.5	13.2	V	(1)
Storage temperature		T _{STG}	-20	60	C	(2)
Glass surface	Center	T _{CENTER}	0	50	C	(0) (5)
temperature (Operation)	T. Uniformity	ΔT	-	10	C	(2),(5)
Shock (non - operating)		S _{nop}	-	50	G	(3)
Vibration (non - operating)		V_{nop}	-	1.5	G	(4)

Note (1) Ta= 25 \pm 2 °C

- (2) Temperature and relative humidity range are shown in the figure below.
 - a. 90 % RH Max. (Ta \leq 39 °C)
 - b. Relative Humidity is 90% or less. (Ta > 39 °C)
 - c. No condensation
- (3) 11ms, sine wave, one time for $\pm X$, $\pm Y$, $\pm Z$ axis
- (4) 10-300 Hz, Sweep rate 10min, 30min for X,Y,Z axis

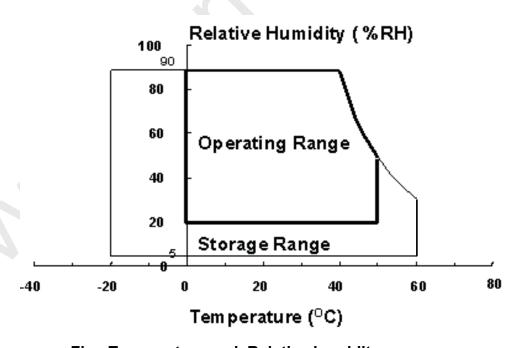


Fig. Temperature and Relative humidity range

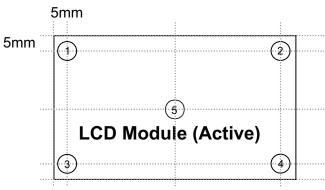
MODEL	LTI400HA03	Doc. No	05-001-G-090513	Page	5 / 27



(5) Definition of test point

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 $\triangle T$ should be less than 10°C ($\triangle T = |T_{CENTER} - T_{CORNER}|$)

T_{CENTER}: Temperature of the center of the glass surface (Test point 5) T_{CORNER}: Temperature of each edge of the glass surface (Test point 1~4)

2. Application information for DID (Digital Information Display)

A long-term display like DID application may cause uneven display including image retention. To optimize module's lifetime and function, several operating usages are required.

- 1. Normal operating condition
- Temperature: 20 ± 15°C
- Humidity: 65 \pm 20 %
- Display pattern: moving picture or regular switchover display

Note) Long-term static information image may cause uneven display.

- 2. Operating usages under abnormal operating condition. Note (1)
 - a. Ambient condition
 - Well-ventilated place is recommended to set up DID system.
 - b. Power off and screen saver
 - Periodical power-off or screen saver is needed after long-term static display. Note (2)
- 3. Operating usages to protect uneven display due to long-term static information display
- a. Suitable operating time for P-DID: under 20 hours a day.
- b. Periodical display contents change from static image to moving picture.
- Liquid crystal refresh time is required.
- c. Periodical background color and character (image) color change
- Use different colors for background and character (image), respectively.
- Change colors periodically.
- d. Avoid combination of background and character with large different luminance.

Note (1) Abnormal condition means every operating condition except normal operating condition.

Note (2) Moving picture or black pattern is strongly recommended for screen saver.

4. Lifetime in this spec is guaranteed only when DID is used under right operating usages.

MODEL LTI400HA03 Doc. No 05-001-G-090513 Page 6	/ 27	
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3. Optical Characteristics

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The optical characteristics should be measured in a dark room or equivalent. Measuring equipment: TOPCON BM-7,SPECTRORADIOMETER SR-3

(Ta = 25
$$\pm$$
 2°C, V_{DD} = 12V, f_{V} = 60Hz, f_{DCLK} = 148.5MHz, I_{L} = 11mArms)

			·			DCLK	<u> </u>	
Item		Symbol	Condition	Min.	Тур.	Max.	Unit	Note
Contrast F (Center of s		C/R		2000	3000	-		(3) SR-3
	Rising	Tr		-	15	20		
Response Time	Falling	Tf		_	6	9	msec	(5) BM-7
7 11110	G-to-G	Tg		-	8	-		DIVI 7
Luminance of (Center of s		Y _L	Normal θ L,R =0	600	700	-	cd/m ²	(6) SR-3
	Red	Rx	θ U,D =0		0.641			
	Red	Ry	Viewing		0.333	\		
	0	Gx	Angle		0.287			
Color Chromaticity (CIE 1931)	Green	Gy		TYP.	0.607	TYP.		(7),(8)
	Dive	Вх		-0.03	0.146	+0.03		SR-3
	Blue	Ву			0.061			
	1A/I-11	Wx			0.280			
	White	Wy			0.290			
Color Ga	mut	-		-	72	-	%	(7) SR-3
Color Temp	erature	-		-	10,000	-	К	(7) SR-3
	l law	θ_{L}		75	89	-	Degree	
Viewing Angle	Hor.	θ_{R}	C/R≥10	75	89	-		(8) SR-3
	1/67	θυ	U/K ≥ 10	75	89	-		
	Ver.	θ_{D}		75	89	-		
Brightness Ui (9 Point		B _{uni}		-	-	25	%	(4) SR-3

Note (1) Test Equipment Setup

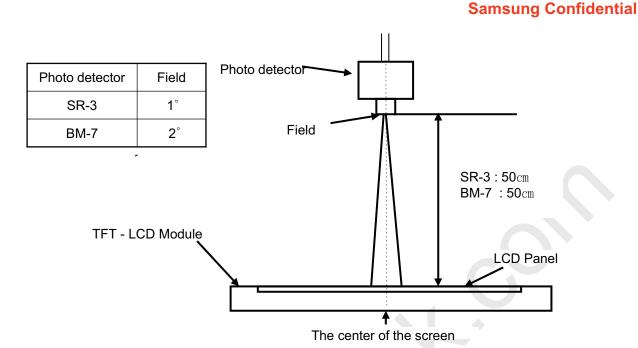
The measurement should be executed in a stable, windless and dark room between 40min and 60min after lighting the backlight at the given temperature for stabilization of the backlight. This should be measured in the center of screen.

Single lamp current: 11mA

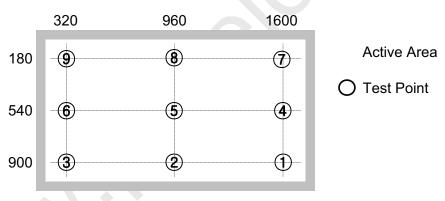
Environment condition : Ta = 25 \pm 2 $^{\circ}$ C

	MODEL	LTI400HA03	Doc. No	05-001-G-090513	Page	7 / 27	
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Note (2) Definition of test point



Note (3) Definition of Contrast Ratio (C/R)

: Ratio of gray max (Gmax) & gray min (Gmin) at the center point ⑤ of the panel

$$C/R = \frac{G \max}{G \min}$$

Gmax: Luminance with all pixels white Gmin: Luminance with all pixels black

MODEL LTI400HA03 Doc. No	05-001-G-090513	Page	8 / 27
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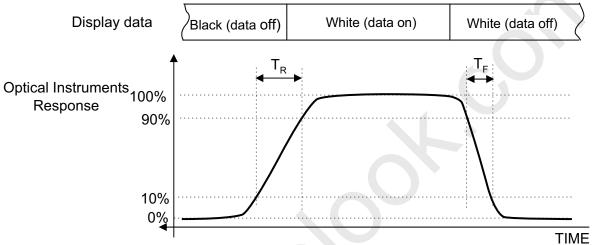
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Note (4) Definition of 9 points brightness uniformity

$$Buni = 100* \frac{(B \max - B \min)}{B \max}$$

Bmax : Maximum brightness Bmin : Minimum brightness

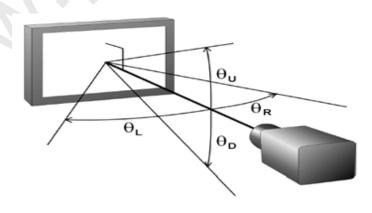
Note (5) Definition of Response time : Sum of Tr, Tf



Note (6) Definition of Luminance of White: Luminance of white at center point ⑤

Note (7) Definition of Color Chromaticity (CIE 1931)
Color coordinate of Red, Green, Blue & White at center point ⑤

Note (8) Definition of Viewing Angle : Viewing angle range (C/R ≥ 10)



MODEL	LTI400HA03	Doc. No	05-001-G-090513	Page	9 / 27
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4. Electrical Characteristics

4.1 TFT LCD Module

The connector for display data & timing signal should be connected.

Ta = 25° C \pm 2 $^{\circ}$ C

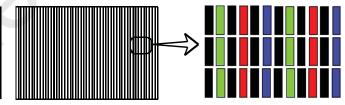
	Item	Symbol	Min.	Тур.	Max.	Unit	Note
Voltage of	Power Supply	V_{DD}	10.8	12.0	13.2	V	(1)
Current	(a) Black		-	600	-	mA	
of Power	(b) White	I _{DD}	-	1000	-	mA	(2),(3)
Supply	(c) N-Pattern		-	1100	1200	mA	
Vsync Free	quency	f _V	45	60	65	Hz	
Hsync Fre	sync Frequency		48.0	67.5	75.0	kHz	
Main Frequency		f _{DCLK}	130.0	148.5	160.0	MHz	
Rush Curr	ent	I _{RUSH}	-	-	4	Α	(4)

Note (1) The ripple voltage should be controlled under 10% of $V_{\rm DD}$.

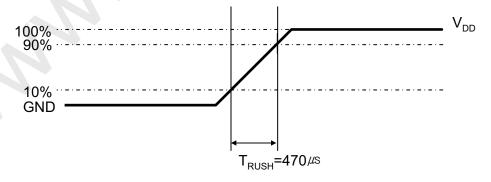
- (2) $f_V = 60$ Hz, $f_{DCLK} = 148.5$ MHz, $V_{DD} = 12.0$ V, DC Current.
- (3) Power dissipation check pattern (LCD Module only)
- a) Black Pattern
- b) White Pattern
- c) N-Pattern







(4) Measurement Conditions



Rush Current I_{RUSH} can be measured when T_{RUSH} . is 470 μ s.

MODEL	LTI400HA03	Doc. No	05-001-G-090513	Page	10 / 27
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4.2 Back Light Unit

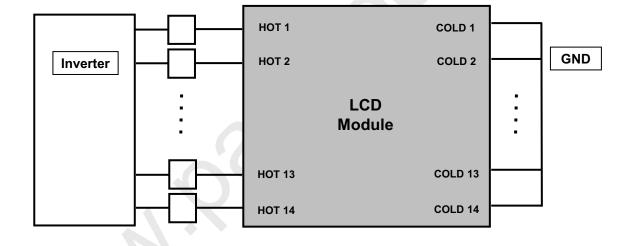
The back light unit contains 14 direct-lighting type CCFTs (Cold Cathode Fluorescent Tube). The characteristics of lamps are shown in the following tables.

Ta=25 \pm 2°C

Item	Symbol	Min.	Тур.	Max.	Unit	Note
Lamp Current	IL	5.0	10.0	12.5	mArms	
Lamp Voltage	V _L	940	990	1110	Vrms	
Operating Life Time	Hr	50,000	-	-	Hour	(1)

Note (1) It is defined as the time to take until the brightness reduces to 50% of its original value.

[Operating condition : Ta = $25\pm2^{\circ}$ C, IL = 10.0mArms, For single lamp only]



MODEL	LTI400HA03	Doc. No	05-001-G-090513	Page	11 / 27

4.3 Inverter Input Condition & Specification

Items	Symbol	Conditions	Sp	ecificatio	ns	Unit	Note
items	Symbol	Conditions	Min.	Тур.	Max.	Offic	Note
Input Voltage	Vin	-	22	24	26	V	Ta=25 ±2 °C
Input Current	lin	Vin=24.0V Vdim=3.3V	-	-	6.6	А	
Lamp Current	I _{O,MAX}	Vdim=3.3V	10.5	11.0	11.5	mArms	After 1 hour Warm-up
Frequency	F_LAMP	Vin=24.0V Vdim=3.3V	41	43	45	kHz	
Backlight	ON	Vin=24.0V	2.4	2.4 -		V	
On/Off	OFF	VIII-24.UV	0	-	0.8	V	-
Dimming	V	Max Lum	3.3		<u> </u>	V	
Control	V_{DIM}	Min. Lum	-		0	V	

Note (1) Power Consumption is measured at 700[cd/m2] of luminance condition which is the typical luminance value. Lamp Current is measured at the point before Lamp.

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MODEL	LTI400HA03	Doc. No	05-001-G-090513	Page	12 / 27



5. Input Terminal Pin Assignment

5.1 Input Signal 8	& Power		Connector : FI-RE51S-HF (JAE)					
PIN No.	Desc	ription	PIN No.	Desc	ription			
1	Vdd	(12V)	26		RE[0]P			
2	Vdd	(12V)	27		RE[1]N			
3	Vdd	(12V)	28		RE[1]P			
4	Vdd	(12V)	29		RE[2]N			
5	Vdd	(12V)	30	Even	RE[2]P			
6	No Cor	nection	31	LVDS	GND			
7	GI	ND	32	Signal	RECLK-			
8	GI	ND	33		RECLK+			
9	GI	ND	34	•	GND			
10		RO[0]N	35		RE[3]N			
11		RO[0]P	36		RE[3]P			
12		RO[1]N	37	No Cor	nection			
13		RO[1]P	38	No Cor	nnection			
14		RO[2]N	39	GI	GND			
15	Odd	RO[2]P	40	No Cor	nnection			
16	LVDS Signal	GND	41	No Cor	nnection			
17		ROCLK-	42	No Cor	nnection			
18		ROCLK+	43	No Cor	nnection			
19		GND	44	No Cor	nnection			
20		RO[3]N	45	LVDS	Option			
21		RO[3]P	46	No Cor	nnection			
22	No Cor	nection	47	No Cor	nnection			
23	No Cor	nnection	48	No Cor	nnection			
24	GI	ND	49	No Cor	nnection			
25	Even LVDS	RE[0]N	50	No Cor	nection			
			51	No Cor	nection			

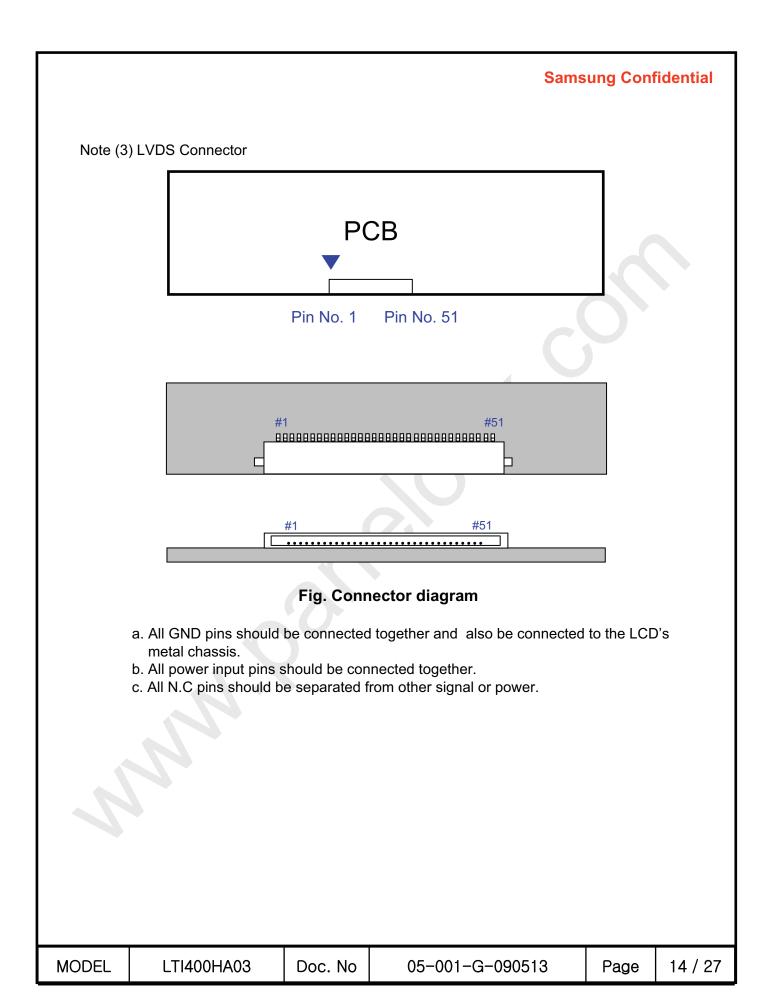
Note(1) No Connection :These pins are only used for SAMSUNG internal purpose.

(2) LVDS Option : High (3.3V) → Normal LVDS format

: Low (GND) or Open (N.C) \rightarrow JEIDA LVDS format

Sequence :On = $VDD(T1) \ge LVDS$ Option $\ge Interface Signal(T2)$ Off = Interface Signal(T3) ≥ LVDS Option ≥ VDD

MODEL	LTI400HA03	Doc. No	05-001-G-090513	Page	13 / 27
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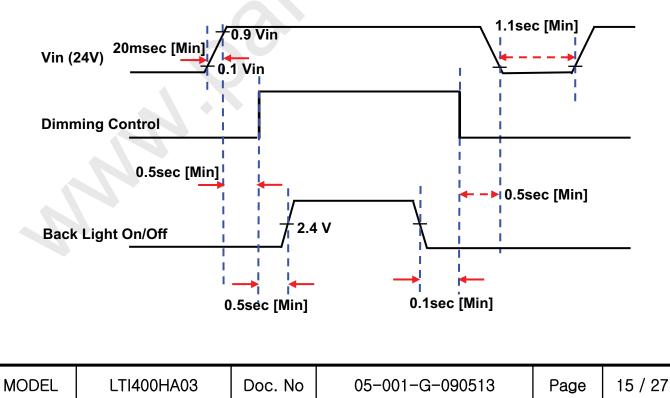


5.2 Inverter Input Pin Configuration

Connector: YEON HO, 20022WR-14B1

Pin No.	Pin Configuration(FUNCTION)
1	Vin (24V)
2	Vin (24V)
3	Vin (24V)
4	Vin (24V)
5	Vin (24V)
6	GND
7	GND
8	GND
9	GND
10	GND
11	No Connection
12	Backlight On /Off [On: 2.4 ~ 5.0V, Off: 0 ~ 0.8V]
13	Dimming Control [0V: Min, 3.3V: Max]
14	No Connection

5.3 Inverter Input Power Sequence





5.4 LVDS Interface

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- Data	a Format (JEIDA	& Normal)		Default LVD	S Option : J	EIDA		
		LVDS pin		JEIDA -DATA	VESA -D	ATA		
		TxIN/RxOU	ТО	R2	R0	R0		
	TxOUT/RxIN0	TxIN/RxOU	T1	R3	R1			
		TxIN/RxOU	T2	R4	R2			
Tx	OUT/RxIN0	TXIN/RXOUT0 R2 R0 TXIN/RXOUT1 R3 R1 TXIN/RXOUT2 R4 R2 TXIN/RXOUT3 R5 R3 TXIN/RXOUT4 R6 R4 TXIN/RXOUT6 R7 R5 TXIN/RXOUT7 G2 G0 TXIN/RXOUT8 G3 G1 TXIN/RXOUT9 G4 G2 TXIN/RXOUT12 G5 G3 TXIN/RXOUT14 G7 G5 TXIN/RXOUT15 B2 B0 TXIN/RXOUT18 B3 B1 TXIN/RXOUT18 B3 B1 TXIN/RXOUT18 B3 B1 TXIN/RXOUT18 B3 B1 TXIN/RXOUT19 B4 B2 TXIN/RXOUT19 B4 B2 TXIN/RXOUT20 B5 B3 TXIN/RXOUT21 B6 B4 TXIN/RXOUT21 B6 B4 TXIN/RXOUT21 B6 B4 TXIN/RXOUT22 B7 B5 TXIN/RXOUT24 HSYNC HSYNC TXIN/RXOUT25 VSYNC VSYNC TXIN/RXOUT26 DEN DEN TXIN/RXOUT27 R0 R6 TXIN/RXOUT27 R0 R6 TXIN/RXOUT5 R1 R7 TXIN/RXOUT10 G0 G6 TXIN/RXOUT11 G1 G7 TXIN/RXOUT11 G1 G7 TXIN/RXOUT11 B1 B7 TXIN/RXOUT17 B1 B7 TXIN/RXOUT17 B1 B7	Т3	R5	R3			
		TxIN/RxOU	T6	R7	R5			
		TxIN/RxOU	Т7	G2	G0			
		TxIN/RxOU	Т8	G3	G1			
		TxIN/RxOU	Т9	G4	G2			
		TxIN/RxOUT	12	G5	G3			
Tx	OUT/RxIN1	TxIN/RxOUT	13	G6	NESA -DATA R0 R1 R2 R3 R4 R5 G0 G1 G2 G3 G4 G5 B0 B1 B2 B3 B4 B5 HSYNC VSYNC VSYNC VSYNC DEN R6 R7 G6 G7 B6 B7 RESERVED			
		TxIN/RxOUT	14	G7				
		TxIN/RxOUT	15	JEIDA -DATA VESA -DATA R2 R0 R3 R1 R4 R2 R5 R3 R6 R4 R7 R5 G2 G0 G3 G1 G4 G2 G5 G3 G6 G4 G7 G5 B2 B0 B3 B1 B4 B2 B5 B3 B6 B4 B7 B5 HSYNC HSYNC VSYNC VSYNC DEN DEN R0 R6 R1 R7 G0 G6 G1 G7 B0 B6 B1 B7 RESERVED RESERVED				
		TxIN/RxOUT	18	B3	B1			
		TxIN/RxOUT	- 19	B4	B2			
	xOUT/RxIN1	TxIN/RxOUT	20	B5	В3	B3		
		TxIN/RxOUT	21	В6	VESA -DATA R0 R1 R2 R3 R4 R5 G0 G1 G2 G3 G4 G5 B0 B1 B2 B3 B4 B5 HSYNC VSYNC DEN R6 R7 G6 G7 B6 B7 RESERVED			
Tx	OUT/RxIN2	TxIN/RxOUT	22	B7				
		TxIN/RxOUT	24	HSYNC				
		TxIN/RxOUT	25	VSYNC		VSYNC		
		TxIN/RxOUT	26	DEN	DEN			
		TxIN/RxOUT	27	R0	R6			
		TxIN/RxOU	T5	R1	R7			
	3	TxIN/RxOUT	10	G0	G6			
Tx	OUT/RxIN3	TxIN/RxOUT	⁻ 11	G1	G7			
		TxIN/RxOUT	16	В0	B6			
		TxIN/RxOUT	17	B1	B7			
		TxIN/RxOUT	RESERVED	RESERV	/ED			



5.5 Input Signals, Basic Display Colors and Gray Scale of Each Color

												D/	ATA S	SIGN	٩L											GRAY
COLOR	DISPLAY (8bit)				RE	ED							GRE	EEN							BL	UE				SCALE
	, ,	R0	R1	R2	R3	R4	R5	R6	R7	G0	G1	G2	G3	G4	G5	G6	G7	В0	В1	B2	ВЗ	В4	B5	В6	В7	LEVEL
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	-
	GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	-
BASIC	CYAN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
COLOR	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	MAGENTA	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	-
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	-
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1 1	1	1	1	1	-
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R0
		1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1
ODAY	DARK	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R2
GRAY SCALE	↑	:	:	:	:	:	:			:	:	:	:	:	: /			:	:	:	:	:	:			R3~
OF RED	RED ↓	:	:	:	:	:	:			:	:	:	:	÷	÷			/ :	:	:	:	:	:			R252
LIGHT	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R253	
		0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R254
	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R255
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G0
		0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G1
GRAY	DARK	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G2
SCALE	<u> </u>	:	:	:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			G3~
OF GREEN	↓	:	:	:	:	:	:				:	:	:	:	:			:	:	:	:	:	:			G252
	LIGHT	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G253
		0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G254
	GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G255
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	В0
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	B1
GRAY	DARK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	B2
SCALE		1:		:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			B3~
OF BLUE	1		:	:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			B252
	LIGHT	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	B253
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	B254
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	B255

Note) Definition of Gray:

Rn : Red Gray, Gn : Green Gray, Bn : Blue Gray (n = Gray level)

Input Signal: 0 = Low level voltage, 1 = High level voltage

MODEL	LTI400HA03	Doc. No	05-001-G-090513	Page	17 / 27	
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6. Interface Timing

6.1 Timing Parameters (DE only mode)

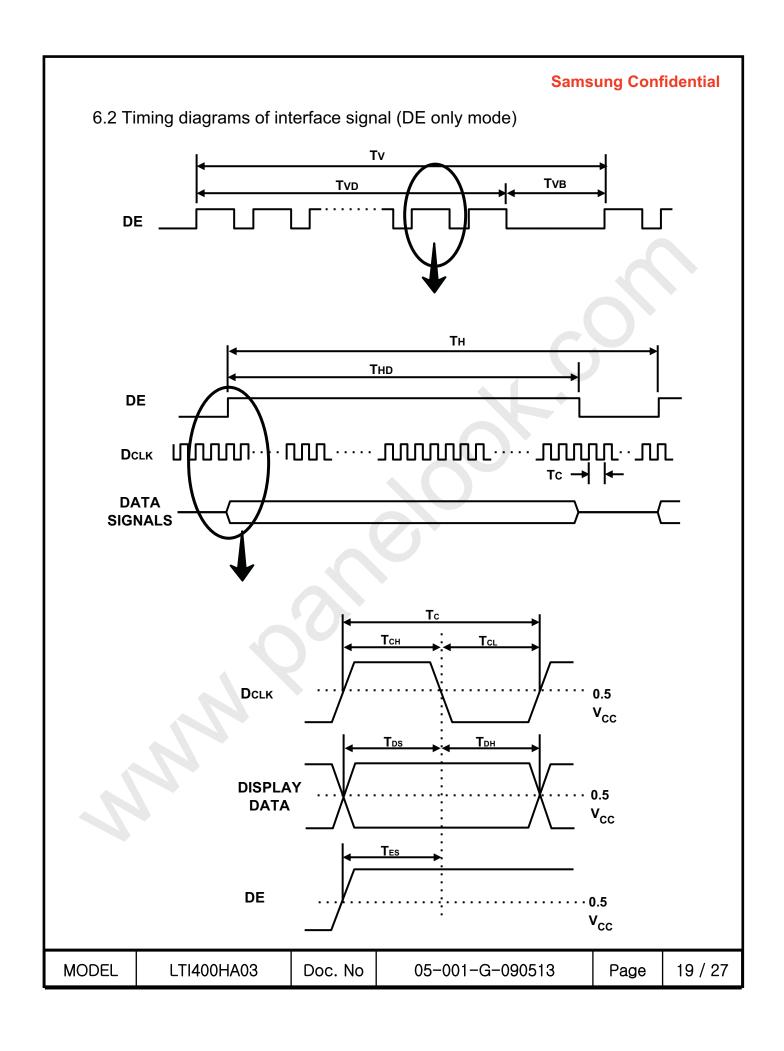
Signal	Item	Symbol	Min.	Тур.	Max.	Unit	Note
Clock		1/T _C	130.0	148.5	160.0	MHz	-
Hsync	Frequency	F _H	48.0	67.5	75.0	KHz	-
Vsync		F _V	45	60	65	Hz	-
Vertical	Active Display Period	T _{VD}	-	1080	-	Lines	-
Display Term	Vertical Total	T _V	1092	1125	1380	Lines	-
Horizontal	Active Display Period	T _{HD}	-	1920	-	Clocks	-
Display Term	Horizontal Total	T _H	2090	2200	2350	Clocks	-

Note) This product is DE only mode. The input of Hsync & Vsync signal does not have an effect on normal operation.

Test Point: TTL control signal and CLK at LVDS Tx input terminal in system

MODEL	LTI400HA03	Doc. No	05-001-G-090513	Page	18 / 27
MODEL	L1140011A00	DOC. NO	05 001 0 030510	lage	10/2/

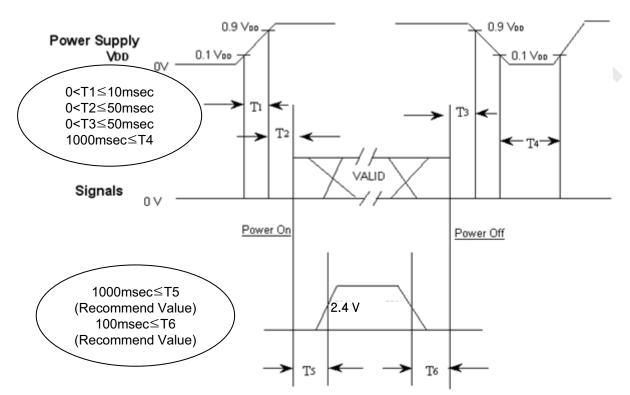






6.3 Power ON/OFF Sequence

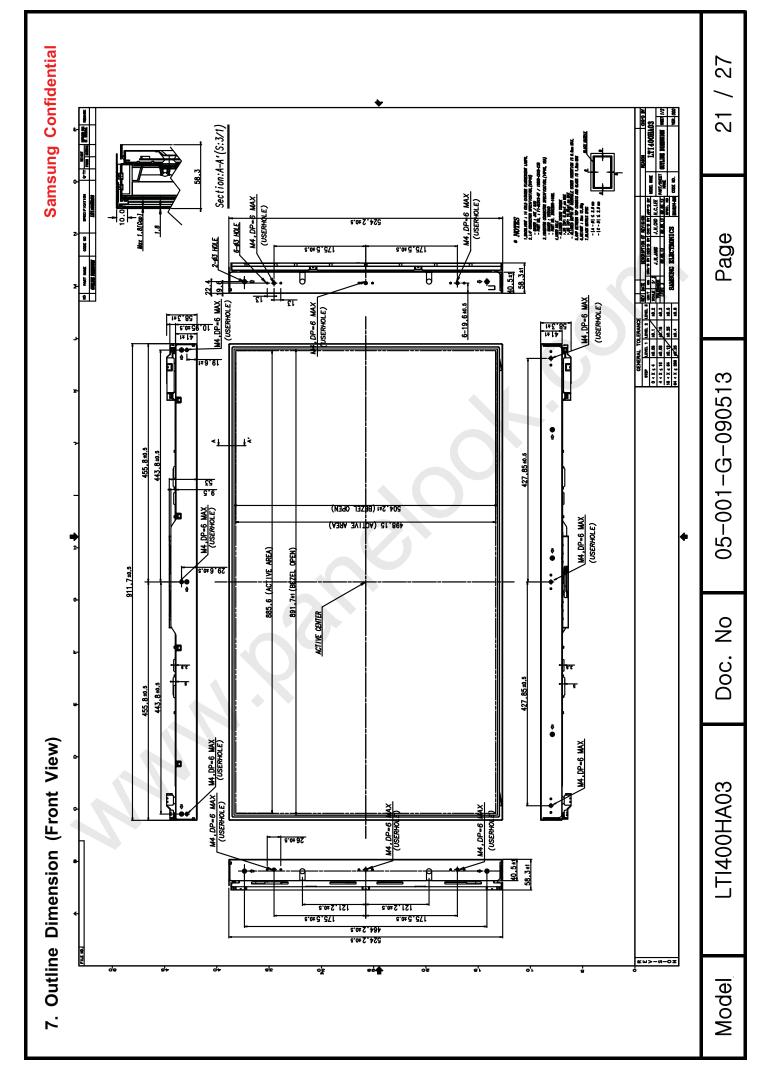
To prevent a latch-up or DC operation of the LCD Module, the power on/off sequence should be as the diagram below.



- T1: V_{DD} rising time from 10% to 90%
- T2 : The time from V_{DD} to valid data at power ON.
- T3 : The time from valid data off to V_{DD} off at power Off.
- T4: V_{DD} off time for Windows restart
- T5: The time from valid data to B/L enable at power ON.
- T6: The time from valid data off to B/L disable at power Off.
- The supply voltage of the external system for the Module input should be the same as the definition of V_{DD}.
- Apply the lamp voltage within the LCD operation range. When the back light turns on before the LCD operation or the LCD turns off before the back light turns off, the display may momentarily show abnormal screen.
- In case of V_{DD} = off level,
 please keep the level of input signals low or keep a high impedance.
- T4 should be measured after the Module has been fully discharged between power off and on period.
- Interface signal should not be kept at high impedance when the power is on.

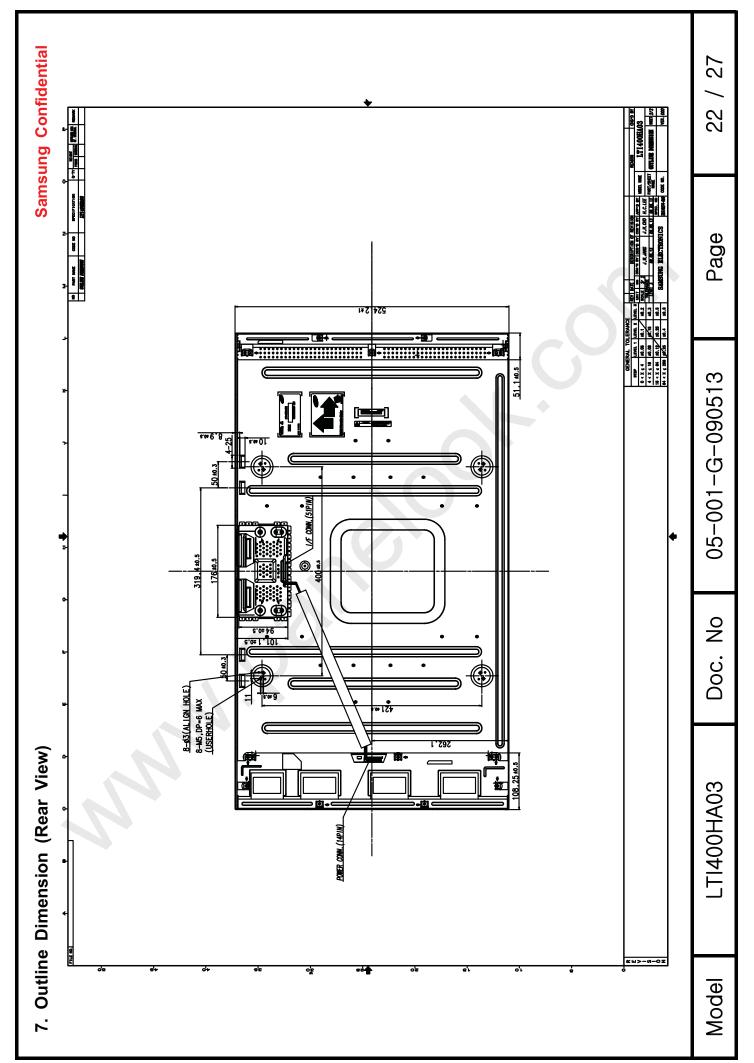
MODEL LTI400HA03 Doc. No	05-001-G-090513	Page	20 / 27
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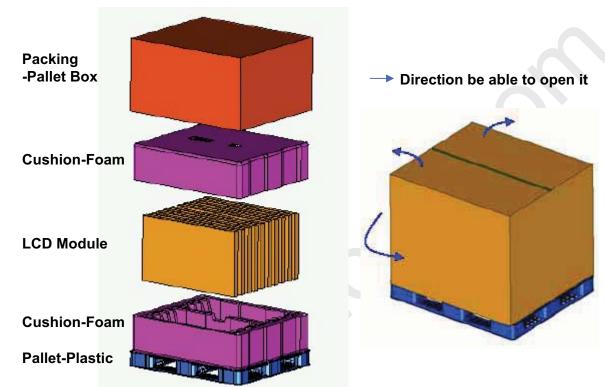
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8. PACKING

- 8.1 CARTON (Internal Package)
 - (1) Packing Form
 Corrugated fiberboard box and corrugated cardboard as shock absorber
 (2) Packing Mathed
 - (2) Packing Method



8.2 Packing Specification

Item	Specification	Remark
LCD Packing	9ea / (Packing-Pallet Box)	1. 108Kg / LCD (9ea) 2. 7 Kg / Cushion-pallet (2ea) 3. 6.7 Kg / Packing-Pallet Box (1ea) 4. Cushion-pallet Material : EPS 5. Packing-Pallet Box Material : DW4
Pallet	1Box / Pallet	1. Pallet weight = 8kg
Packing Direction	Vertical	
Total Pallet Size	H x V x height	1150mm(H) x 985mm(V) x 609mm(height)
Total Pallet Weight	129.7 kg	Pallet(8kg) + Module(108 kg) + Cushion(7kg) + Pallet-BOX(6.7kg)

MODEL	LTI400HA03	Doc. No	05-001-G-090513	Page	23 / 27
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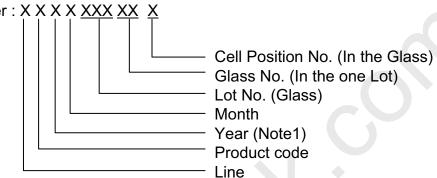
9. MARKING & OTHERS

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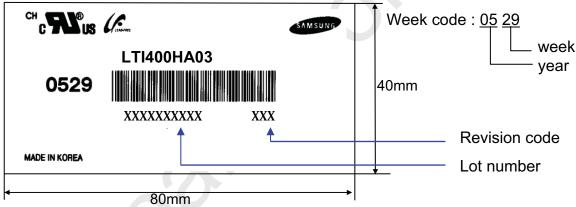
A nameplate bearing followed by is affixed to a shipped product at the specified location on each product.

(1) Part number: LTI400HA03 (2) Revision: Three letters

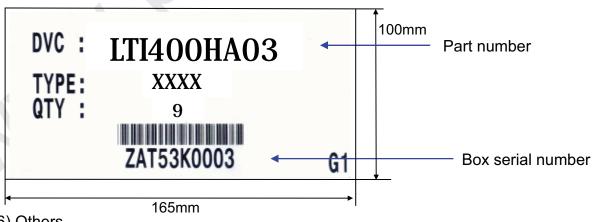
(3) Lot number: X X X X X XXX XX XX



(4) Nameplate Indication



(5) Packing box attach



(6) Others

1. After service part Lamps cannot be replaced because of the narrow bezel structure.

MODEL	LTI400HA03	Doc. No	05-001-G-090513	Page	24 / 27
4					



10. General Precautions

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10.1 Handling

- (a) When the Module is assembled, it should be attached to the system firmly using all mounting holes. Be careful not to twist and bend the Module.
- (b) Because the inverter use high voltage, it should be disconnected from power before it is assembled or disassembled.
- (c) Refrain from strong mechanical shock and / or any force to the Module. In addition to damage, this may cause improper operation or damage to the Module and CCFT backlight.
- (d) Note that polarizers are very fragile and could be damage easily. Do not press or scratch the surface harder than a HB pencil lead.
- (e) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, staining or discoloration may occur.
- (f) If the surface of the polarizer is dirty, clean it using absorbent cotton or soft cloth.
- (g) Desirable cleaners are water, IPA(Isopropyl Alcohol) or Hexane. Do not use Ketone type materials(ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
- (h) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, legs or clothes, it must be washed away with soap thoroughly.
- (i) Protect the Module from static, or the CMOS Gate Array IC would be damaged.
- (j) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (k) Do not disassemble the Module.
- (I) Do not adjust the variable resistor located on the Module.
- (m) Protection film for polarizer on the Module should be slowly peeled off just before use so that the electrostatic charge can be minimized.
- (n) Pins of I/F connector should not be touched directly with bare hands.

MODEL LTI400HA03 Doc. No 05-001	G-090513 Page 25 / 27
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10.2 Storage

- (a) Do not leave the Module in high temperature, and high humidity for a long time. It is highly recommended to store the Module with temperature from 0 to $35\,^{\circ}$ C and relative humidity of less than 70%.
- (b) Do not store the TFT-LCD Module in direct sunlight.
- (c) The Module should be stored in a dark place. It is prohibited to apply sunlight or fluorescent light in storing.

10.3 Operation

- (a) Do not connect or disconnect the Module in the "Power On" condition.
- (b) Power supply should always be turned on/off by the "Power on/off sequence"
- (c) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference should be done by system manufacturers.Grounding and shielding methods may be important to minimize the interference.
- (d) The cable between the backlight connector and its inverter power supply should be connected directly with a minimized length. A longer cable between the backlight and the inverter may cause lower luminance of lamp(CCFT) and may require higher startup voltage(Vs).

10.4 Operation Condition Guide

(a) The LCD product should be operated under normal conditions. Normal condition is defined as below:

- Temperature : 20±15 °C

- Humidity : $55\pm20\%$

- Display pattern : continually changing pattern (Not stationary)

(b) If the product will be used in extreme conditions such as high temperature, humidity, display patterns or operation time etc.., It is strongly recommended to contact SEC for Application engineering advice. Otherwise, its reliability and function may not be guaranteed. Extreme conditions are commonly found at Airports, Transit Stations, Banks, Stock market, and Controlling systems.

MODEL	LTI400HA03	Doc. No	05-001-G-090513	Page	26 / 27	
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10.5 Others

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- (a) Ultra-violet ray filter is necessary for outdoor operation.
- (b) Module should be turned clockwise (regular front view perspective) when used in portrait mode
- (c) Avoid condensation of water. It may result in improper operation or disconnection of electrode.
- (d) Do not exceed the absolute maximum rating value. (supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on)
 Otherwise the Module may be damaged.
 - Otherwise the Module may be damaged.
- (e) If the Module keeps displaying the same pattern for a long period of time, the image may be "sticked" to the screen.To avoid image sticking, it is recommended to use a screen saver.
- (f) This Module has its circuitry PCB's on the rear side and should be handled carefully in order not to be stressed.
- (g) Please contact SEC in advance when you display the same pattern for a long time.

MODEL	LTI400HA03	Doc. No	05-001-G-090513	Page	27 / 27